

In the Specification

Please replace the cross-reference list in paragraphs [0003] to [0008] with the updated list, as follows:

[0003] United States patent application number 6,757,774 entitled, "Redundant, High-Availability, Highly-Redundant Storage System Enclosure" (HP Docket No. 10003435-1), naming Anthony J. Benson and James J. deBlanc as inventors and filed on even date herewith;

[0004] United States patent application number 6,668,294 entitled, "System and Method for Data Corruption Avoidance on a Backplane Bus Adapted to Receive Bus Controller Cards of Different Types" (HP Docket No. 10003436-1), naming Anthony J. Benson and Patrick McGoey as inventors and filed on even date herewith;

[0005] United States patent application number 6,748,477 entitled, "Multiple-Path Interface Card for Interfacing Multiple Isolated Interfaces to a Storage System" (HP Docket No. 10003437-1), naming Anthony J. Benson and James J. deBlanc as inventors and filed on even date herewith;

[0006] United States patent application number 6,789,151 entitled, "DIP Switch Configuration for Increased Usability with Multiple Cards" (HP Docket No. 10003438-1), naming Anthony J. Benson, Chadi Theodosy, and Joanna Baisden as inventors and filed on even date herewith;

[0007] United States patent application number 6,715,019 entitled, "Bus Reset Management of Communication Bus Resets by a Primary Controller Card of Multiple Controller Cards" (HP Docket No. 10005621-1), naming Anthony J. Benson, James L. White, and Dovard K. Howard as inventors and filed on even date herewith; and

[0008] United States patent application number 6,675,242 entitled, "Master-Slave Communication Bus Controllers Controller Including Designation of Primary and Secondary Status According to Slot Position" (HP Docket No. 10013519-1), naming Anthony J.

Benson,, James L. White, and Dovard K. Howard as inventors and filed on even date herewith.